

PERSONAL INFORMATIONS

Andrea Baschirotto

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 Sex M | Born on July, 23th, 1965 | Nationality Italian

**CURRENT POSITION
& AFFILIATION**

Full Professor (since 1/11/2020)
 University of Milan-Bicocca (UniMiB)
 Department of Physics ‘G. Occhialini’
 Piazza della Scienza, 3
 20126 Milan (Italy)

Concourse Sector: 09/E3 Electronics
 SSD: ING-INF/01

EDUCATION

(1994) Phd in Electronics
 University of Pavia, Pavia (Italy)
(1989) Master Degree in Electronics Engineering (110/110 summa cum laude)
 University of Pavia, Pavia (Italy)

RESEARCH INTERESTS

Analog electronics
 Microelectronics – Integrated circuits
 Analog and Mixed-signal Integrated circuits design
 Analog design in scaled technologies

LANGUAGES

MOTHER LANGUAGE Italian
 OTHER LANGUAGE English

PROFESSIONAL CAREER
ACADEMIC POSITIONS AND COMMITMENTS

(2024-2028) Adjunct Professor at University of Electronic Science and Technology of China
(2019-2022) Adjunct Professor at University of Electronic Science and Technology of China
(2017-2020) & (2020-2022) & (2023-2025) Member
 Gruppo di Lavoro per la Ricerca e la Valorizzazione della Ricerca – Univ. of Milan-Bicocca
(2008 – today) Director (& Founder) – Univ. of Milan-Bicocca
 Microelectronics Group (up to >20 people and raised >3M€ funding)
(2008 – today) Member of Faculty of Physics / Department of Physics ‘G. Occhialini’ – Univ. of Milan-Bicocca
 Responsible of the courses of Electronics for Bachelor, Master and PhD students
(2007 – today) Associate Professor - Univ. of Milan - Bicocca, Milan (Italy)
(2000 – 2007) Director (& Founder) – Univ. of Lecce/Salento, Lecce (Italy)
 Microelectronics Group (up to > 15 people and raised >2.5M€ funding)
(1998 – 2007) Member of Faculty of Engineering / Department of Innovation Engineering – Univ. of Lecce
 Responsible of the courses of Electronics for Bachelor, Master and PhD students
(2007 – 2020) Associate Professor - University of Milan-Bicocca, Milan (Italy)
(1998 – 2007) Associate Professor - University of Lecce/Salento, Lecce (Italy)
(1994 – 1998) Assistant Professor - University of Pavia, Pavia (Italy)

TEACHING ACTIVITIES

ACADEMIC COURSES	<p>(2008-today) Univ. of Milan-Bicocca – Associate professor <i>Bachelor courses:</i> Elements of Electronics (SSD ING-INF/01) <i>Master courses:</i> Electronics Lab, Electronics Lab I, Electronics Lab II, Microelectronics, Microelectronics Lab, Digital Electronics, Industrial Electronics, Electronic Instrumentation (all in SSD ING-INF/01)</p> <p>(2008-2012) Univ. of Lecce/Salento – Contract professor <i>Master course:</i> Microelectronics (SSD ING-INF/01)</p> <p>(2004) Univ. of Modena – Contract professor <i>Bachelor courses:</i> Electronics A (SSD ING-INF/01)</p> <p>(1998-2007) Univ. of Lecce - Associate professor <i>Bachelor courses:</i> Analog Electronics, Electronics I (all in SSD ING-INF/01) <i>Master courses:</i> Electronics for Telecommunication, Microelectronics (all in SSD ING-INF/01)</p>
THESIS SUPERVISOR	<p>PhD Thesis Tutor of >20 PhD thesis (11 running)</p> <p>Master Thesis Tutor of >80 Master thesis</p>

AWARDS & SCHOLARSHIP

SCHOLARSHIP	<p>(1990-91) One-year fellowship (Italian) National Program for Microelectronics</p>
AWARDS	<p>(2021) Best Student Paper Award International Conference on Biomedical Electronics and Devices (BIODEVICES/BIOSTEC)</p> <p>(2020) Best Paper Award IEEE Custom Integrated Circuits Conference (CICC2020)</p> <p>(2015) Best student paper Award European Solid-State Circuits Conference (ESSCIRC2015)</p> <p>(2008) Top Article list IEEE - Transactions on Circuits & Systems - I</p> <p>(2006) Best paper Award European Solid-State Circuits Conference (ESSCIRC2006)</p> <p>(2005-24) Several Leaf Awards for PhD student research activities IEEE PhD Research in Microelectronics Conference (PRIME)</p>

SERVICES TO PROFESSION

CULTURAL ASSOCIATION	<p>(2018 – today) Founder & President Cultural Association 'InnotechEvents'</p> <p>(2006 – 2018) Founder & President Cultural Association 'MicroElectronicsEvents'</p>
INDUSTRIAL COMMITMENT	<p>(2024-now) – Director of the MicroElectronics Division STEindustries (Milan – Italy)</p> <p>(2018-2019) Visiting Scientist Synaptics (California – USA)</p> <p>(2015 – 2017) - Chairman International Technical Board - Semitrex (Stati Uniti d'America)</p> <p>(2014) Responsible μTLAB – Spin-off company approved & co-funded by Italian Ministry</p> <p>(2013) Visiting Scientist Microelectronics Competence Center of Ireland - MCCI (Ireland)</p> <p>(2012-2017) Visiting Scientist Conexant (California – USA)</p> <p>(2012–2017) Technical Steering Group (Ireland) Member Microelectronics Competence Center of Ireland - MCCI (Ireland)</p> <p>(2010-2011) Visiting Scientist STEricsson (California – USA)</p> <p>(2009) Visiting Scientist NXP (California – USA)</p> <p>(2007 – 2008) Visiting Scientist Glonav (California – USA)</p>

- EDITORSHIP**
- (2020) Guest Editor**
IEEE Solid State Circuits - Letter Journal – Special issue for ESSCIRC2020
 - (2014 – 2018) Associate Editor**
IEEE Journal of Solid-State Circuits
 - (2013) Guest Editor**
IEEE Solid State Circuit Society Magazine for 2014 Summer edition
 - (2006 – today) Founder & President**
IEEE Solid-State Circuit Society - Italian Chapter
 - (2003 – 2005) Associate Editor**
IEEE Transaction on Circuits & Systems (part I)
 - (2003) Guest Editor**
IEEE Journal of Solid-State Circuits – Special issue for ESSCIRC2002
 - (2000 – 2003) Associate Editor**
IEEE Transaction on Circuits & Systems (part II)

- TECHNICAL PROGRAM COMMITTEE MEMBERSHIP**
- (2011- Today) Technical Program Committee Member**
Workshop on 'Advances on Analog Circuit Design Workshop' (AACD)
 - (2008-2009) European Sub-Committee Secretary**
International Solid-State Circuit Conference (ISSCC)
 - (2006-2015) Steering Committee Member**
IEEE PhD Research in Electronics and Microelectronics Conference (PRIME)
 - (2005-Today) Data-Converter Sub-Committee Chairman**
European Solid-State Circuit Conference (ESSCIRC)
 - (2004-2011) Technical Program Committee Member**
International Solid-State Circuit Conference (ISSCC)
 - (1992-Today) Technical Program Committee - Member**
European Solid-State Circuit Conference (ESSCIRC)

- SCIENTIFIC EVENT ORGANIZATION**
- (2024) Local Organizer**
Workshop on Advances in Analog Circuit Design (AACD2024, Pavia – Italy)
 - (2023) General Chairman**
Workshop on Advances in Analog Circuit Design (AACD2023, Villach – Austria)
 - (2022) General Chairman**
European Solid State Device Research Conference & European Solid State Circuits Conference (ESSDERC/ESSCIRC2022, Milan – Italy)
 - (2019) General Chairman**
Workshop on Advances in Analog Circuit Design (AACD2019, Milan – Italy)
 - (2016) Local Organizing Chairman**
Advances in Analog Circuit Design (AACD2016, Villach – Austria)
 - (2013) General Chairman**
IEEE-PRIME2013 (Villach/Austria)
 - (2013) Local Organizing Chairman**
Workshop on Advances in Analog Circuit Design (AACD2013, Grenoble – France)
 - (2013) Local Organizing Chairman**
IEEE International Conference on Integrated Circuits Technology and Design (ICICDT2013 Pavia – Italy)
 - (2007- Today) General Director**
IEEE 'Topics on Microelectronics' Courses (SSCS Italian Chapter)
 - (2009) General Chairman**
AISEM2009 (Pavia – Italy)
 - (2008) General Chairman**
GE2008 (Otranto – Italy)
 - (2008) Local Organizing Chairman**
Workshop on Advances in Analog Circuit Design (AACD2008, Pavia – Italy)
 - (2006) General Chairman**
IEEE - PRIME2006 (Otranto/Italy)
 - (2002) Technical Program Committee Chairman**
ESSCIRC2002 (Florence/Italy) (the largest participation edition)

- COMMISSION OF TRUST**
- (1996) Secretary for research proposal evaluation process**
ASI (Italian Spatial Agency)
 - (2004) Evaluator**
National Research, Technological Development and Innovation (RTDI) - Programme of Malta State

(2010) Evaluator

Swedish Foundation for Strategic Research (SSF)

(2011) External Review

MCCI (Microelectronics Competence Centre Ireland)

(2011) Evaluator

Italian Research Ministry for the PRIN proposal

(2011) Evaluator

Italian Research Ministry for the FIRB proposal

(2012) Evaluator

Research Projects for the Call "Young Researchers of Cyprus"

(2012) Evaluator

Research Projects for the Dutch technology foundation (STW) - The Netherlands

(2015) Evaluator

Methusalem Program – Belgium

(2022) Member

List of the proposers for 2023 Nobel Prize in Physics

SCIENTIFIC SOCIETY MEMBERSHIP

(1988) IEEE Student Member

(2001) IEEE Senior Member

(2014) IEEE Fellow Member

RESEARCH PROJECTS & GRANTS

ON-GOING RESEARCH PROJECTS

FULL RESPONSIBLE (P.I.)

(2019) Brain28 (PRIN projected funded by MIUR)

Autonomous In-vivo Brain-Machine-Interface in 28nm-CMOS technology with Ultrasound-based Power-Harvester and Communication-Link (Brain28nm) – Partner: Univ. Napoli, Univ. Catania, Univ. Padova

PAST 10 YEARS RESEARCH PROJECTS

FULL RESPONSIBLE (P.I.)

(2018) finFeT16 (INFN Group V Experiment)

Radiation damage effects in 16nm-finFeT technology – Partner: INFN-Pavia, INFN-Torino, INFN-Padova, CERN-Ginevra/Svizzera, CSEM-Neuchatel/Svizzera

(2016) AniThing (PRIN projected funded by MIUR)

Advanced Nanometer IC Technologies for Next Generation Transceivers (ANiThiNG) – Partner: Univ.Pavia, Univ. Napoli

(2015) ScalTech28 (INFN Group V Experiment)

Radiation damage effects in 28nm-bulk CMOS technology – Partner: INFN-Pavia, INFN-Padova, CERN-Ginevra/Svizzera, CSEM-Neuchatel/Svizzera

ON-GOING INDUSTRIAL GRANTS

FULL RESPONSIBLE (P.I.)

(2022) SensorIP (Industrial Grant from Infineon-Austria)

Design of sensor IP blocks for audio applications in deep submicron CMOS technologies

(2021) ScalTechDAC (Grant from University of Pavia-Italy)

Progettazione di un Convertitore Digitale Analogico e dei circuiti di test in tecnologia scalata

(2021) MotorControl (Industrial Grant from Infineon-Italy)

Advanced architecture and circuits for motor control applications

(2021) LPADCMedical (Industrial Grant from icAlps-France)

Ultra-low-power ADC Hybrid Architecture for medical applications

(2021) MixSignSmartPower (Industrial Grant from Infineon-Austria)

Development of high-performance mixed signal processing stages in monolithic Automotive Smart-Power technology

(2020) AdvSensor (Industrial Grant from Infineon-Austria)

Advance Sensor Building Blocks

(2019) InnoPowMan (Industrial Grant from Infineon-Italy)

Innovative design technics applied to Power Management

(2019) PowLineCom (Industrial grant from Melexis-Germany)

Supply line embedded communication in automotive sensor/actuator networks

PAST 10 YEARS GRANTS

FULL RESPONSIBLE (P.I.)

(2018) HybridADC (Industrial grant from Infineon-Austria)

Hybrid ADC development of multisensory platform

(2018) Power2020 (Industrial grant from Infineon-Austria)

Advanced design techniques in High-Power technologies

(2018) PowerControl (Industrial grant from Infineon-Italia)

Advanced architecture and circuits for high-voltage power control

(2018) PowerManagement (Industrial grant from Infineon-Italia)

Innovative design techniques applied to Power Management
 (2016) Sens (Industrial grant from Infineon-Austria)
 LV-LP sensor interface
 (2015) AnalogVAD (Industrial grant from Conexant/Synaptics-USA)
 Ultra-low-power analog VAD system
 (2015) LVMPH (Industrial grant from Infineon-Austria)
 LV-LP smart sensor interface analog design
 (2015-2016) LPCTSDM (Conexant) Ultra-low power analog circuit for audio applications
 (2014-2017) EM2APS (Infineon) High-performance analog design in power IC technology
 (2014) National Ministry - Recipient of spin-off proposal co-funding - L. 593
 (2013-2014) LV-LP analog design (Research Grant from Tronchetti-Provera Foundation)
 (2013) MUON detector for ATLAS experiment (Max Plank Institute – Munich-Germany)

PAST 10 YEARS GRANTS
 LOCAL UNIT RESPONSIBLE

(2019) UltimateGaN (H2020-ECSEL European project) - UniMiB Unit Responsible
 Research for GaN technologies, devices, packages and applications to address the challenges of the future GaN roadmap
 (2019) Lombardia Regional project ‘Pignoletto’ – Electronics ADC Design Responsible
 (2018) iDev-4.0 (H2020-ECSEL European project) - UniMiB Responsible
 Integrated development 4.0
 (2016) Lombardia Regional project ‘The Home of Internet of Things’ (HomeIoT)
 (2015) RD-Phase-2 (INFN) - 28nm IC develop Responsible
 (2014) BEAMforFUSION (INFN) - LV-LP analog design (front-end for GEM detectors)
 (2013) LSPE (LSPE collaboration) - Control ASIC for cryogenic MMIC amplifier for space experiment (LSPE)
 (2013) Lombardia Regional project on Air Pollution - SMELLER

RESEARCH COLLABORATIONS (WITH PUBLICATIONS
 AND/OR PATENTS)

Accent (Italy), Acco (Italy), Chipidea (Portugal), Conexant (USA), IMEC (Belgium), IMEC-NL (The Netherland), Infineon (Italy, and Austria), Intersil (India), Mindspeed (USA), Marvell (Italy), Medico (Italy), Mikron (Germany), RFDomus (USA), GloNav (USA), NXP (USA), STMicroelectronics (Italy, France), Silicon Biosystems (Italy), ASTRI (Hong-Kong), CNR-IMM (Italy), “Bruno Kessler” Foundation (Italy), INFN (Italy) for HEP experiments (L3, AMS, RD35), Bari Polytechnic (Italy), Milan Polytechnic (Italy), Turin Polytechnic (Italy), Bologna University (Italy), Ferrara University (Italy), LAquila University (Italy), Padua University (Italy), Pavia University (Italy), Roma-2 “Tor Vergata” University (Italy)

AB Participated to several High-Energy Physics (HEP) collaborations, among them L3 with spokesman prof. S. Ting (1976 Nobel Prize winner).

PUBLICATIONS NUMBERS

PUBLICATIONS	Books	1
	Books chapters	21
	Book Editor	15
	International Journal papers	195
	Conference paper (with referee)	342
	HEP experiments papers	124
US PATENTS	US Patents	54
BIBLIOGRAPHIC H-INDEX (MARCH, 8TH, 2023)	Scopus:	42 (documents 617, citations 8.198)
	Web of Knowledge:	42 (documents 539, citations 6.894)
	GoogleScholar:	56 (citations 14.896)

LISTS OF PUBLICATIONS

BOOKS

2007

[B.1] V. Giannini, J. Craninckx, A. Baschirotto, “**Baseband Analog Circuits for Software Defined Radio**”, Springer, The Netherlands, 2007

BOOK CHAPTERS 1999

[Bc.1] A. Baschirotto, R. Castello, "**1V Switched-capacitor filters**", 6th chapter in "*Advances in Analog circuit design*", Editors: J. Huijsing, R. van der Plassche, and W. Sansen, Kluwer Academic Press, 1999 - pp.69-91

[Bc.2] A. Baschirotto, "**Switched-capacitor filters**", "*The VLSI handbook*", Editor: Wai-Kai Chen, CRC Press and IEEE Press, New York 2000, pp. 59.1-59.32

2001

[Bc.3] A. Baschirotto, "**Low-Voltage Switched-Capacitor Filters**", Chapter no. 10, in "*Low-power design techniques and CAD tools for analog and RF integrated circuits*", Editors: P. Wambacq, G. Gielen, J. Gerrits, Kluwer Academic Publishers, London, 2001

[Bc.4] A. Baschirotto, "**Trade-off in Switched-Capacitor Circuits**", in "*Understanding trade-offs in analog circuits: a designer's companion*", Editors: C. Toumazou, G.S. Moschytz, B. Gilbert, Kluwer Academic Publishers, pp.443-459

2003

[Bc.5] A. Baschirotto, "**Low-voltage Switched-capacitor Filters**", in "Low-Power Design Techniques and CAD Tools for Analog and RF Integrated Circuits", Pages 215-250

2004

[Bc.6] A. Baschirotto, M. Sala, F. Salidu, F. Stefani, "**IF A/D Converters for a DSP-based FM-receiver**", 18th chapter in "*Advances in Analog circuit design*", Kluwer Academic Press, 2004 - pp.69-91

2005

[Bc.7] A. Baschirotto, S. D'Amico, P. Malcovati, "**Low-Voltage, Low-Power Basic Circuits**", 18th chapter in "*Advances in Analog circuit design*", Kluwer Academic Press, 2005 - pp.295-327

2006

[Bc.8] A. Baschirotto, S. D'Amico, M. De Matteis, "**Advances on Analog Filters for Telecommunications**", 5th chapter in "*Advanced signal Processing, Circuits, and System Design Techniques for Communications*", Editors: V. Paliouras, Th. Stouraitis, and A. Ioinovici, IEEE Press, 2006 - pp.131-168

[Bc.9] A. Baschirotto, "**Switched-capacitor filters**", "*The VLSI handbook – 2nd edition*", Editor: Wai-Kai Chen, CRC Press and IEEE Press, New York 2006

2009

[Bc.10] S. D'Amico, O. Rousseaux, K. Philips, B. Gyselinck, A. Baschirotto, "**Ultra Wide Band in Medical Applications**", in "Advances in Biomedical Sensing, Measurements, Instrumentation and Systems", Springer, Dec. 2009, pp. 43-60

2010

[Bc.11] M. Grassi, P. Malcovati and A. Baschirotto, "**Fundamental Limitations in Resistive Wide-Range -Sensor Interface Circuits Design**", Sensors and Microsystems, Lecture Notes in Electrical Engineering, Vol. 54, Part 1, Springer, pp.25-40

[Bc.12] S. D'Amico, A. Baschirotto, M. De Matteis, F. Grancagnolo, M. Panareo, R. Perrino, G. Chiodini, A. Corvaglia, "**A 0.13 μm CMOS front-end for drift chambers**", Sensors and Microsystems, Lecture Notes in Electrical Engineering, Vol. 54, Springer, pp. 295-298

[Bc.13] S. D'Amico, M. De Matteis, A. Baschirotto, "**Source-Follower-Based Continuous Time Analog Filters**", Analog Circuit Design, Springer 2010, Pages 167-187

2011

[Bc.14] F. Conso, M. Grassi, P. Malcovati and A. Baschirotto, "**A Very Large Dynamic Range Integrated Interface Circuit for Heterogeneous Resistive Sensors Matrix Read-Out**", Lecture Notes in Electrical Engineering, 1, Volume 109, Sensors and Microsystems, Part 5, Pages 271-277

[Bc.15] A. Baschirotto, V. Colonna, G. Gandolfi, "**Oversampled DACs**", Chapter 12 in "Analog Circuit Design", Springer 2011, Pages 227-255

2013

[Bc.16] P. Malcovati, M. Grassi, A. Baschirotto, "**Interface Circuits for MEMS Microphones**", Chapter 9, pp. 149-174 in "Nyquist AD Converters, Sensor Interfaces, and Robustness", Springer, 2013

2015

[Bc.17] F. Conso, M. Grassi, C. De Berti, P. Malcovati, A. Baschirotto, "**Above 160dB Dynamic-Range -Sensor-Grid Front-end Integrated Circuit with 500 C, 1.5**

C/Pitch Temperature Gradient Synthesis, 20-Channel MUX, and I2C Interface”,
Sensors, pp. 297-301

[Bc.18] M. De Matteis, A Baschirotto, **“Continuous Time Analog Filters Design in Nanometerscale CMOS Technologies”,** Chapter 8 in “High-performance AD and DA Converters, IC Design in Scaled Technologies, and Time-domain Signal Processing”, Springer, 2015

2016

[Bc.19] R. Castello, C. De Berti, and A. Baschirotto, **“Linearization Techniques for Push-Pull Amplifiers”,** Chapter 8 in [BE.11] **“Efficient Sensor Interfaces, Advanced Amplifiers and Low Power RF Systems”,** (2016) – Springer publisher

2019

[Bc.20] L. Crespi, C. De Berti, B. Friend, P. Malcovati, and A. Baschirotto, **“Low Power Microphone Front-Ends”,** Chapter 17 in **“Low-Power Analog Techniques, Sensors for Mobile Devices, and Energy Efficient Amplifiers”,** (2019) – Springer publisher

2020

[Bc.21] M. De Matteis, F. Resta, A. Pipino, F. Fary, S. Mattiazzo, C. Eaz, A. Baschirotto, **“1-GRad-TID Effects in 28-nm Device Study for Rad-Hard Analog Design”,** Chapter 18 in **“Next-Generation ADCs, High-Performance Power Management, and Technology Considerations for Advanced Integrated Circuits”,** (2020) – Springer publisher

BOOKS EDITOR 1996

[BE.1] **“1st Region 8 IEEE-CAS Workshop on Analog and Mixed IC Design”,** Pavia, 13-14 Settembre 1996 - A. Baschirotto, V. Liberali, F. Maloberti (Editors)

1997

[BE.2] **“2nd Region 8 IEEE-CAS Workshop on Analog and Mixed IC Design”** Baveno, 14-15 Settembre 1997 - A. Baschirotto, V. Liberali, F. Maloberti (Editors)

2000

[BE.3] **“New trends in CMOS RF transceivers”,** A. Baschirotto (Editor) - La Goliardica Pavese - Pavia, 2000

2002

[BE.4] **“Proceedings of the European Solid-State Circuits Conference – ESSCIRC2002”,** A. Baschirotto, P. Malcovati (Editors), Le Frontieres, 2002

2006

[BE.5] **“Proceedings of the Conference on PhD. Research in MicroElectronics – PRIME2006”** - A. Baschirotto, P. Malcovati (Editors) - Otranto (Italia), 2006

2010

[BE.6] **“Sensors and Microsystems”,** Malcovati, P. (Ed), Baschirotto, A. (Ed), d'Amico, A. (Ed), Natale Di, C. (Ed) (2010) – Springer publisher

2012

[BE.7] **“Analog Circuit Design”,** Steyaert, M. (Ed), van Roermund, A. (Ed), Baschirotto, A. (Ed) (2012) – Springer publisher

2013

[BE.8] **“Nyquist AD Converters, Sensor Interfaces, and Robustness”,** van Roermund, A. H. (Ed), Baschirotto, A. (Ed), Steyaert, M. (Ed) (2013) – Springer publisher

2014

[BE.9] **“Frequency References, Power Management for SoC, and Smart Wireless Interfaces”,** Baschirotto, A. (Ed), Makinwa, K. A. (Ed), Harpe, P. (Ed) (2014) – Springer publisher

2015

[BE.10] **“High-Performance AD and DA Converters, IC Design in Scaled Technologies, and Time-Domain Signal Processing”,** Harpe, P. (Ed), Baschirotto, A. (Ed), Makinwa, K. A. A. (Ed) (2015) – Springer publisher

2016

[BE.11] **“Efficient Sensor Interfaces, Advanced Amplifiers and Low Power RF Systems”,** Makinwa, K. A. (Ed), Baschirotto, A. (Ed), Harpe, P. (Ed) (2016) – Springer publisher

2017

[BE.12] **“Wideband Continuous-time $\Sigma\Delta$ ADCs, Automotive Electronics, and Power Management”,** Baschirotto, A. (Ed), Harpe, P. (Ed), Makinwa, K. A. A. (Ed) (2017) – Springer publisher

2018

[BE.13] **"Hybrid ADCs, Smart Sensors for the IoT, and Sub-1V & Advanced Node Analog Circuit Design"**, Harpe, P. (Ed), Makinwa, K. A. A. (Ed), Baschirotto, A. (Ed) (2018) – Springer publisher

2019

[BE.14] **"Low-Power Analog Techniques, Sensors for Mobile Devices, and Energy Efficient Amplifiers"**, Makinwa, K. A. A. (Ed), Baschirotto, A. (Ed), Harpe, P. (Ed) (2019) – Springer publisher

2020

[BE.15] **"Next-Generation ADCs, High-Performance Power Management, and Technology Considerations for Advanced Integrated Circuits"**, Baschirotto, A. (Ed), Harpe, P. (Ed), Makinwa, K. A. A. (Ed) (2020) – Springer publisher

PAPERS IN INTERNATIONAL JOURNALS

1991

[P.1] A.Baschirotto, R.Alini, R.Castello, **"BiCMOS operational amplifier with precise and stable dc-gain for high-frequency switched-capacitor filters"**, IEE Electronics Letters - 18th July 1991 - pp.1338-1340

[P.2] A.Baschirotto, M.Cassis, P.Kirchlechner, F.Montecchi, G.Palmisano, D.Rossi, **"An analog BiCMOS integrated circuit for front-end RDS decoder"**, IEEE Transactions on Consumer Electronics - August 1991 - pp.585-591

1992

[P.3] A.Baschirotto, R.Castello, F.Montecchi, **"Zero-placement technique for IIR switched-capacitor anti-aliasing decimators"**, International Journal on Circuit Theory and Applications - Jan.-Feb. 1992 - pp.87-91

[P.4] A.Baschirotto, R.Castello, F.Montecchi, **"IIR double-sampled switched-capacitor decimators for high-frequency applications"**, IEEE Transactions on Circuits and Systems - April 1992 - pp.300-304

[P.5] A.Baschirotto, R.Castello, F.Montecchi, **"Finite gain compensated double-sampled switched-capacitor integrator for high-Q bandpass filters"**, IEEE Transactions on Circuits and Systems - June 1992 - pp.425-431

[P.6] A.Baschirotto, R.Castello, A.Gola, G.Pessina, P.G.Rancoita, M.Rattaggi, G.Terzi, **"Slew-rate enhancement techniques to be used with silicon calorimeters charge sensitive preamplifiers"**, IEE Electronics Letters - 22nd October 1992 - pp.2109-2110

[P.7] R.Alini, A.Baschirotto, R.Castello, **"Tunable BiCMOS continuous-time filters for high-frequency applications"**, IEEE Journal of Solid-State Circuits - December 92 - pp.1905-1915 appears also in **"BiCMOS integrated circuit design"**, edited by M. I. Elmasry - IEEE Press - pp.442-452

1993

[P.8] R.Castello, A.Baschirotto, **"Analog blocks in telecommunications systems"**, Annales des Telecommunications - July-August 1993 - pp.413-419

[P.9] A.Baschirotto, M.Bosetti, R.Castello, A.Gola, P.G.Rancoita, M.Rattaggi, M.Redaeli, A.Seidman, G.Terzi, **"Development of a new monolithic fast preamplifier to be used in LHC radiation environment"**, Nuclear Physics B (Proc. Suppl. 3rd Intern. Conf. on Advanced Technology and Particle Physics - Como, Villa Olmo '92) - 1993 - pp.535-539

1994

[P.10] A.Baschirotto, R.Castello, F.Montecchi, **"Design strategy for low-voltage SC circuits"**, IEE Electronics Letters - 3rd March 1994 - pp.378-379

[P.11] A.Baschirotto, R.Castello, G.Pessina, P.G.Rancoita, M.Rattaggi, A.Gola, M.Redaeli, **"Monolithic bandpass RC-CR filter for high-energy physics experiments"**, IEE Electronics Letters - 28th April 1994 - pp.691-692

[P.12] M.Segato, A.Baschirotto, G.Biaggioni, G.Cortelazzo, E.Malavasi, **"SC realization of anti-aliasing CCIR-601 video filters"**, IEEE Transactions on Consumer Electronics - August 1994 - pp.274-281

[P.13] A.Baschirotto, F.Rezzi, R.Castello, **"A low-voltage balanced transconductor with high input common-mode rejection"**, IEE Electronics Letters - 29th September 1994 - pp.1669-1671

1995

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